IN THE SPECIFICATION

Please amend the specification as follows:

Related Applications

This application is related to ~~U.S. Patent Application No.~~ _____ ~~, entitled "Multistream Processing System and Method", filed on even date herewith; to U.S. Patent Application No.~~ _____ ~~, entitled "System and Method for Synchronizing Memory Transfers", Serial No.~~ _____ ~~, filed on even date herewith; to~~ U.S. Patent Application No.[[_____]] 10/643,742, entitled "Decoupled Store Address and Data in a Multiprocessor System", filed on even date herewith; to U.S. Patent Application No. [[_____]] 10/643586, entitled "Decoupled Vector Architecture", filed on even date herewith; to U.S. Patent Application No.[[_____]] 10/643,585, entitled "Latency Tolerant Distributed Shared Memory Multiprocessor Computer", filed on even date herewith; to U.S. Patent Application No.[[_____]] 10/643,754, entitled "Relaxed Memory Consistency Model", filed on even date herewith; to U.S. Patent Application No. [[_____]] 10/643,758, entitled "Remote Translation Mechanism for a Multinode System", filed on even date herewith; and to U.S. Patent Application No.[[_____]] 10/643,741, entitled "~~Method and Apparatus for Local Synchronizations in a Vector Processor System~~ Multistream Processing Memory-And Barrier-Synchronization Method And Apparatus", filed on even date herewith, each of which is incorporated herein by reference.